Name:

PRN No:

**High Performance Computing Lab**

**Practical No. 8**

**Title of practical:** Implementation of Vector-Vector addition & N-Body Simulator using CUDA C

**Problem Statement 1:**

Implement Vector-Vector addition using CUDA C. State and justify the speedup using different size of threads and blocks.

**Screenshots:**

**Output:**

Speedup analysis: Tabular and Graphical

**Problem Statement 2:**

Implement N-Body Simulator using CUDA C. State and justify the speedup using different size of threads and blocks.

**Screenshots:**

**Output:**

Speedup analysis: Tabular and Graphical